

What is claimed is:

1. A liquid crystal display, comprising:

a first substrate, said first substrate including:

a transparent substrate;

5 a plurality of gate lines and a plurality of drain lines formed on said transparent substrate;

thin film transistors, each of said thin film transistors being provided in a vicinity of the intersection of said gate line and drain line;

10 pixel electrodes, each of said pixel electrodes being formed within a pixel region enclosed with an adjacent pair of said gate lines and an adjacent pair of said drain lines, and connected to said thin film transistor; and

15 common electrodes, each of said common electrodes developing an electric field within each of said pixel regions between said pixel electrode and itself;

a second substrate opposing to said first substrate, said second substrate including color layers provided for each of said pixel regions, said color layers being spaced apart from said gate lines and drain lines when seen on a plane; and

liquid crystal provided in a space between said first substrate and second substrate.

2. A liquid crystal display, comprising,

25 a first substrate, said first substrate including:

a transparent substrate;

a plurality of gate lines and a plurality of drain lines formed on said transparent substrate;

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thin film transistors, each of said thin film transistors being provided in a vicinity of the intersection of said gate line and drain line;

5 pixel electrodes, each of said pixel electrodes being formed within a pixel region enclosed with an adjacent pair of said gate lines and an adjacent pair of said drain lines, and connected to said thin film transistor; and

10 common electrodes, each of said common electrodes developing an electric field within each of said pixel regions between said pixel electrode and itself;

15 a second substrate opposing to said first substrate, said second substrate including color layers provided for each of said pixel regions, said color layers being spaced apart from said gate lines and overlapping said drain lines when seen on a plane; and

liquid crystal provided in a space between said first substrate and second substrate.

20 3. The liquid crystal display according to claim 2, wherein each adjacent pair of said color layers across a drain line among said drain lines are overlapped with each other above said drain line therebetween.

25 4. The liquid crystal display according to claim 1, further comprising a driving circuit which applies signal voltages of opposite polarities to each adjacent pair of pixels, respectively, while inverting polarities of said gate lines per scanning cycle.

5. The liquid crystal display according to claim 2,

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further comprising a driving circuit which applies signal voltages of opposite polarities to each adjacent pair of pixels, respectively, while inverting polarities of said gate lines per scanning cycle.

5 6. The liquid crystal display according to claim 3, further comprising a driving circuit which applies signal voltages of opposite polarities to each adjacent pair of pixels, respectively, while inverting polarities of said gate lines per scanning cycle.

10 7. The liquid crystal display according to claim 1, wherein each of said pixel electrodes and common electrodes is of a comb-teeth-wise shape.

15 8. The liquid crystal display according to claim 2, wherein each of said pixel electrodes and common electrodes is of a comb-teeth-wise shape.

 9. The liquid crystal display according to claim 3, wherein each of said pixel electrodes and common electrodes is of a comb-teeth-wise shape.

20 10. The liquid crystal display according to claim 4, wherein each of said pixel electrodes and common electrodes is of a comb-teeth-wise shape.

 11. The liquid crystal display according to claim 5, wherein each of said pixel electrodes and common electrodes is of a comb-teeth-wise shape.

25 12. The liquid crystal display according to claim 6, wherein each of said pixel electrodes and common electrodes is of a comb-teeth-wise shape.

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